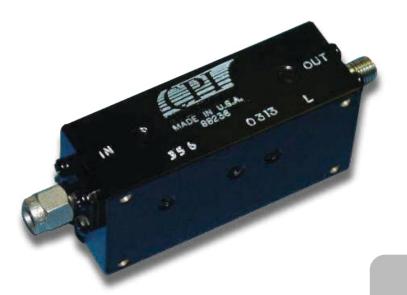
Communications & Power Industries Limiter



With a history of producing high quality products, we can help you with your limiter.

Contact us at BMDMarketing@cpii.com or call us at +1 978-922-6000.

FEATURES

- Broad bandwidth
- Wide pulse width
- High duty cycle

BENEFITS:

- World's largest manufacturer of receiver protectors
- State of the art facility with high level of vertical integration
- Extensive high power test capability
- In-house environmental test facility
- Computer modeling and automatic test capabilities

APPLICATIONS:

- Missile seekers
- Airborne radars
- Unmanned Aerial Vehicles (UAV)
- Ground based systems
- Naval radars
- Air traffic control radars
- Weather radar



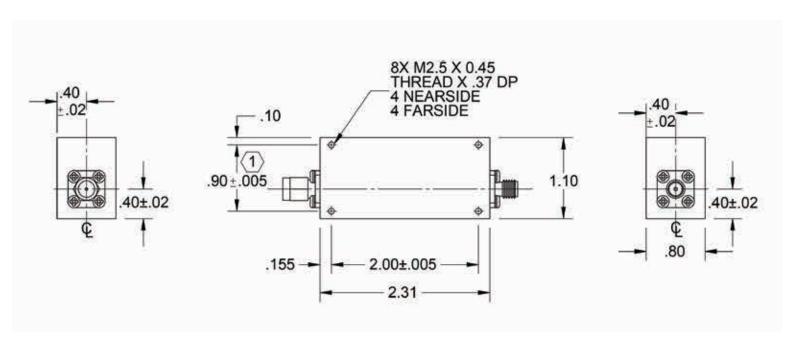
CPI L-Band 4 kW Passive Limiter: VLL2021

Electrical Specifications

Licetifical openinoations	
Operating frequency	1.2 – 1.4 GHz
Maximum normal operating power conditions	Power: 10 W peak Pulse: 1 millisecond. Duty cycle: 36%
Maximum overload power conditions	Power: 4 kW peak Pulse: 40 µSec Duty cycle: 1%
Minimum input RF risetime	100 μSec
Maximum insertion loss -40 to +55 C +50 to +70 C Maximum input return loss -40 to +55 C +50 to +70 C Maximum spike leakage	0.4 dB 0.55 dB 19.08 dB 15.6 dB 23 dBm
power (active) Maximum flat leakage power	20 dBm
Maximum recovery time (to 0.1 dB under normal operating conditions)	15 μSec

Mechanical and Environmental **Specifications**

RF input	SMA Male
RF output	SMA Female
Dimensions	See outline drawing
Operating temperature -40° to +70° C	
Storage temperature	-40° to +85° C
Maximum humidity	95%
Shock	50 g, 11 mSec pulse, ½ sine wave, 1 axis, 3 pulses
Vibration	MIL-STD-810F, Method 514.5, Category 24 3 axes, I hr per axis
Maximum operating altitude	10,000 feet





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For more detailed information, please refer to the corresponding CPI technical description if one has been published, or contact CPI. Specifications may change without notice as a result of additional data or product refinement. Please contact CPI before using this information for swetner design. for system design.

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